

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re: Appn. Ser. No. 10/072,346

Art Unit 1623

Filed 2/6/02

Exr. Not Yet Assigned

Inventors Coteus et al

Atty Dkt No. YOR919980423US2

1503
5A
10/4/02



For: ADDRESS WRAP FUNCTION FOR ADDRESSABLE MEMORY DEVICES

PRELIMINARY AMENDMENT

RECEIVED

OCT 30 2002

Assistant Commissioner for Patents
U.S. Patent and Trademark Office
Washington, D.C. 20231
Sir:

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TECH CENTER 1600/2900

Kindly preliminarily amend the above identified application as follows:

In the claims:

Kindly add the following additional claims:

- 1 21. In a data processing memory assembly of the type having an input port, an output port, and containing
- 2 information stored in a plurality of addressable storage locations,
- 3 said memory assembly being responsive in processing to data having an address

4 information portion and a to be stored portion,

5 the improvement comprising

- 6 said processing including a selective capability of directing said address information portion
- 7 of said data appearing in a data path to said plurality of addressable storage locations in a separate
- 8 processing path without disturbing said information stored in said plurality of
- 9 addressable storage locations.

1 22. The improvement of claim 21 wherein said selective capability includes

- 2 a separate address information path around said addressable storage locations to said output port, and,
- 3 a processing instruction implementing the directing of said address information portion of said data to said
- 4 separate path.